

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

JUN-HYUN CHUN

Application No.:

Filed:

For: **memory chip architecture having
non-rectangular memory banks and method
for arranging memory banks**

Art Group:

Examiner:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

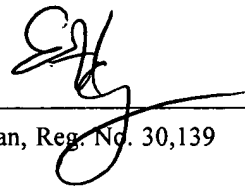
TRANSMITTAL OF FORMAL DRAWINGS

Sir:

Enclosed herewith for filing in the above-identified U.S. Patent Application are the formal drawings, 16 sheets including 17 Figures. Please charge any additional fees or credit any overpayment to Deposit Account No. 02-2666. A duplicate copy of the Fee Transmittal is enclosed for this purpose.

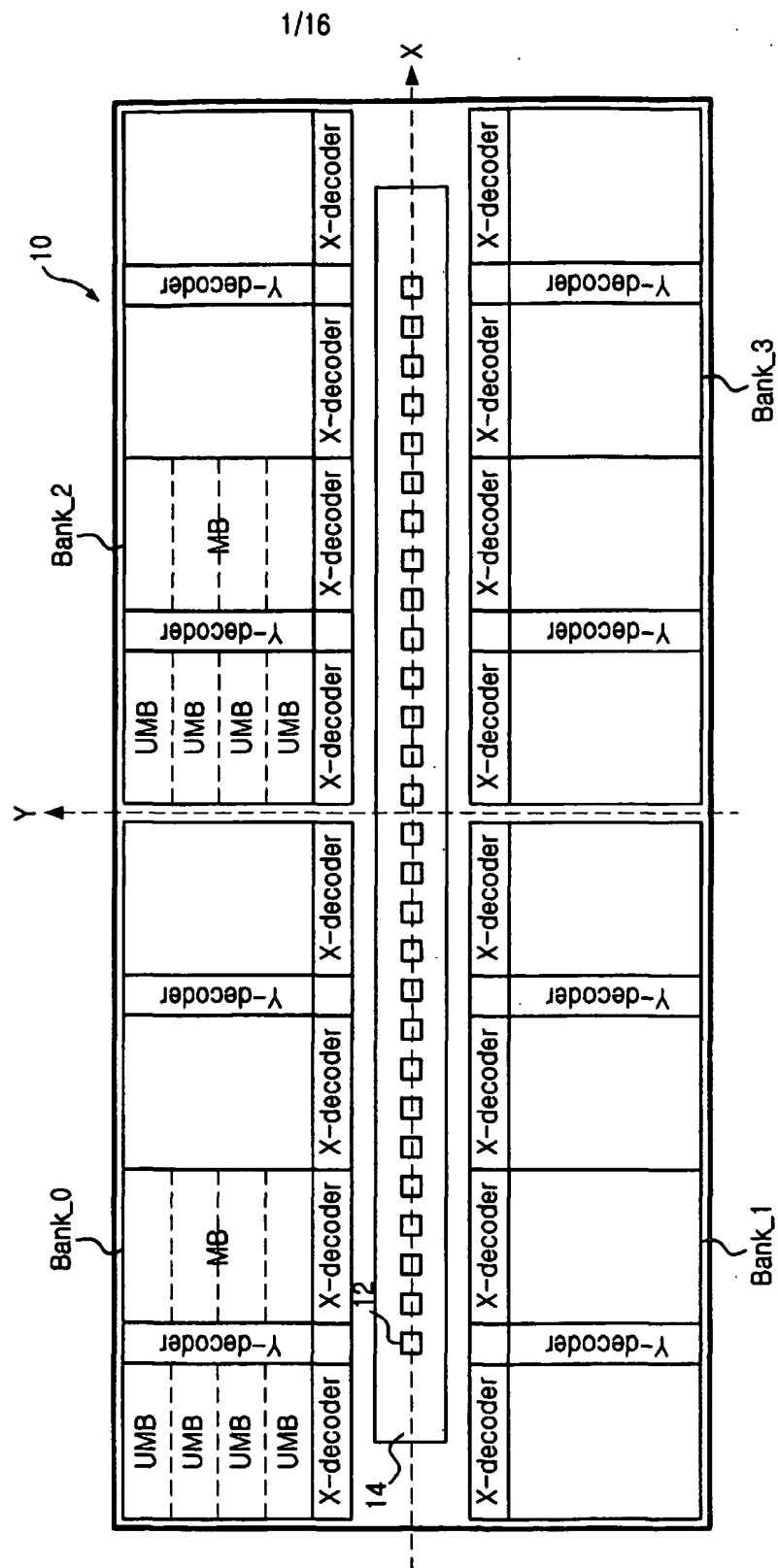
Respectfully submitted,
Blakely, Sokoloff, Taylor & Zafman LLP

Dated: 10/3/04


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FIG. 1
(PRIOR ART)



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FIG. 2
(PRIOR ART)

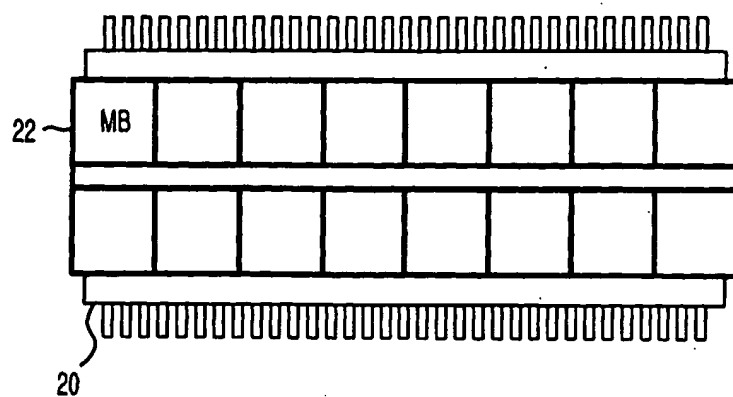


FIG. 3

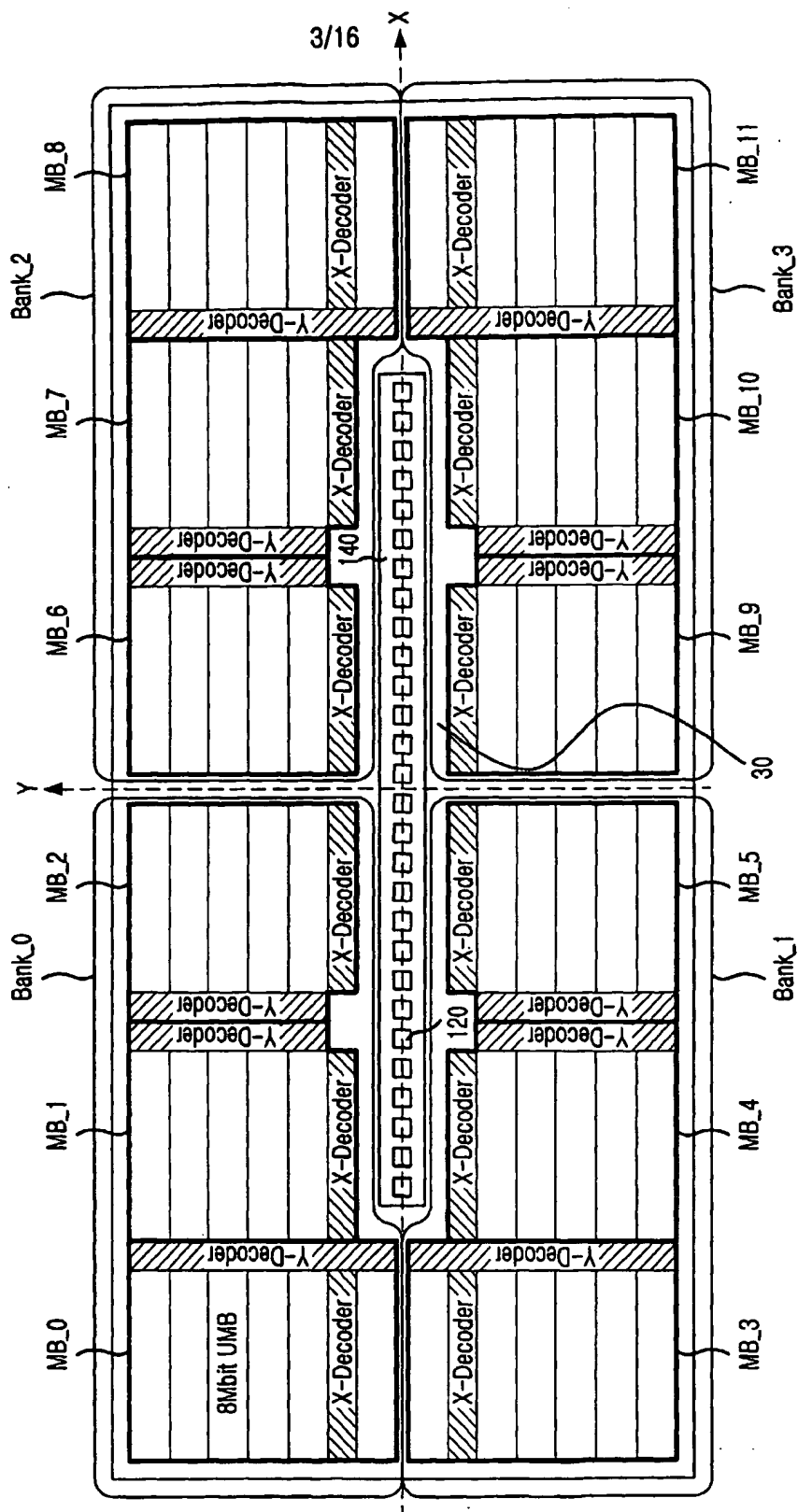
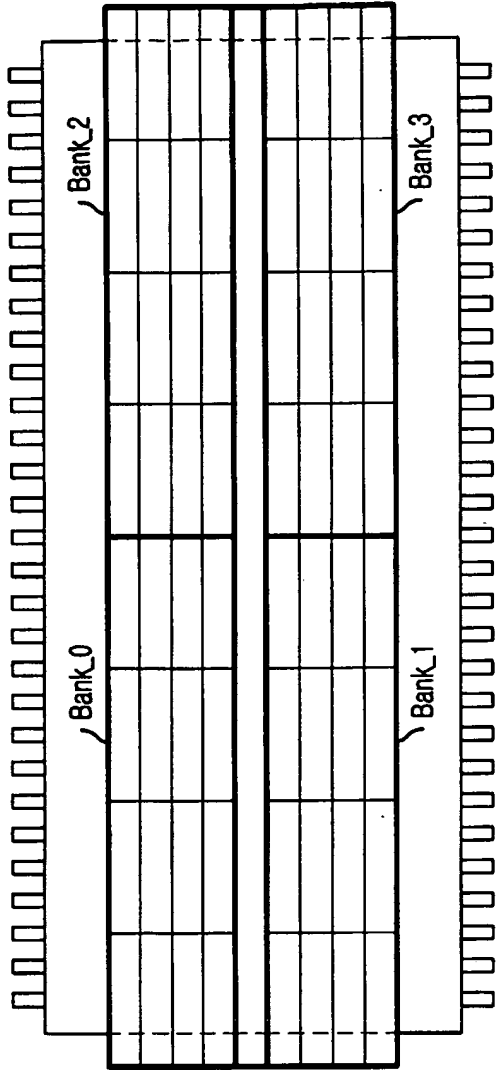


FIG. 4A



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FIG. 4B

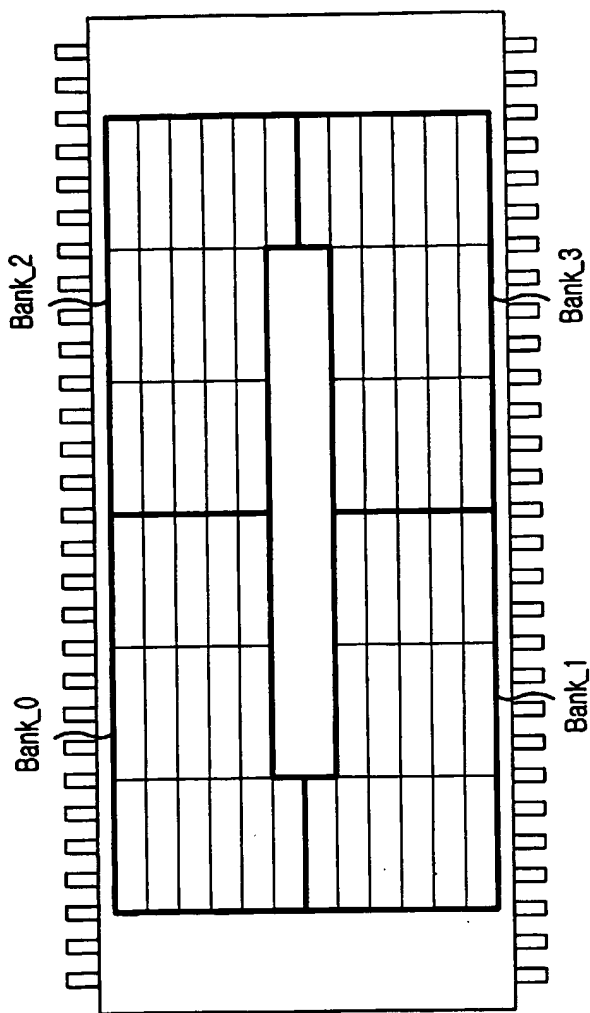


FIG. 5A

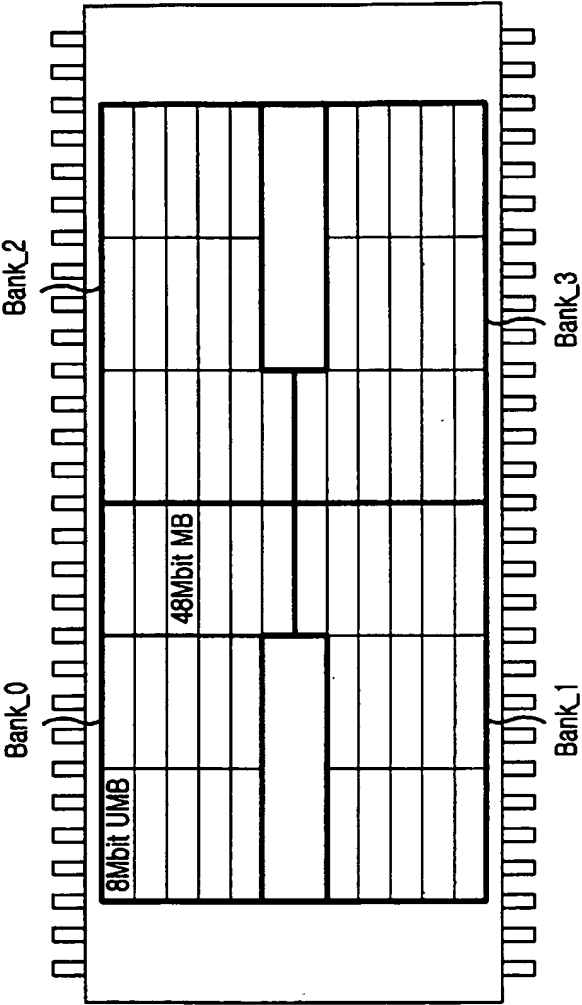


FIG. 5B

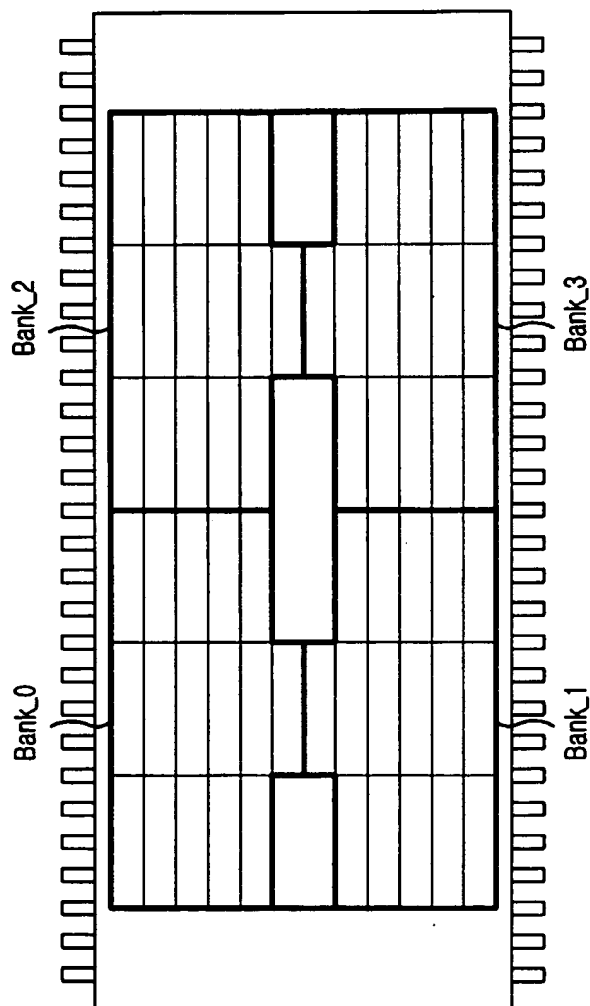
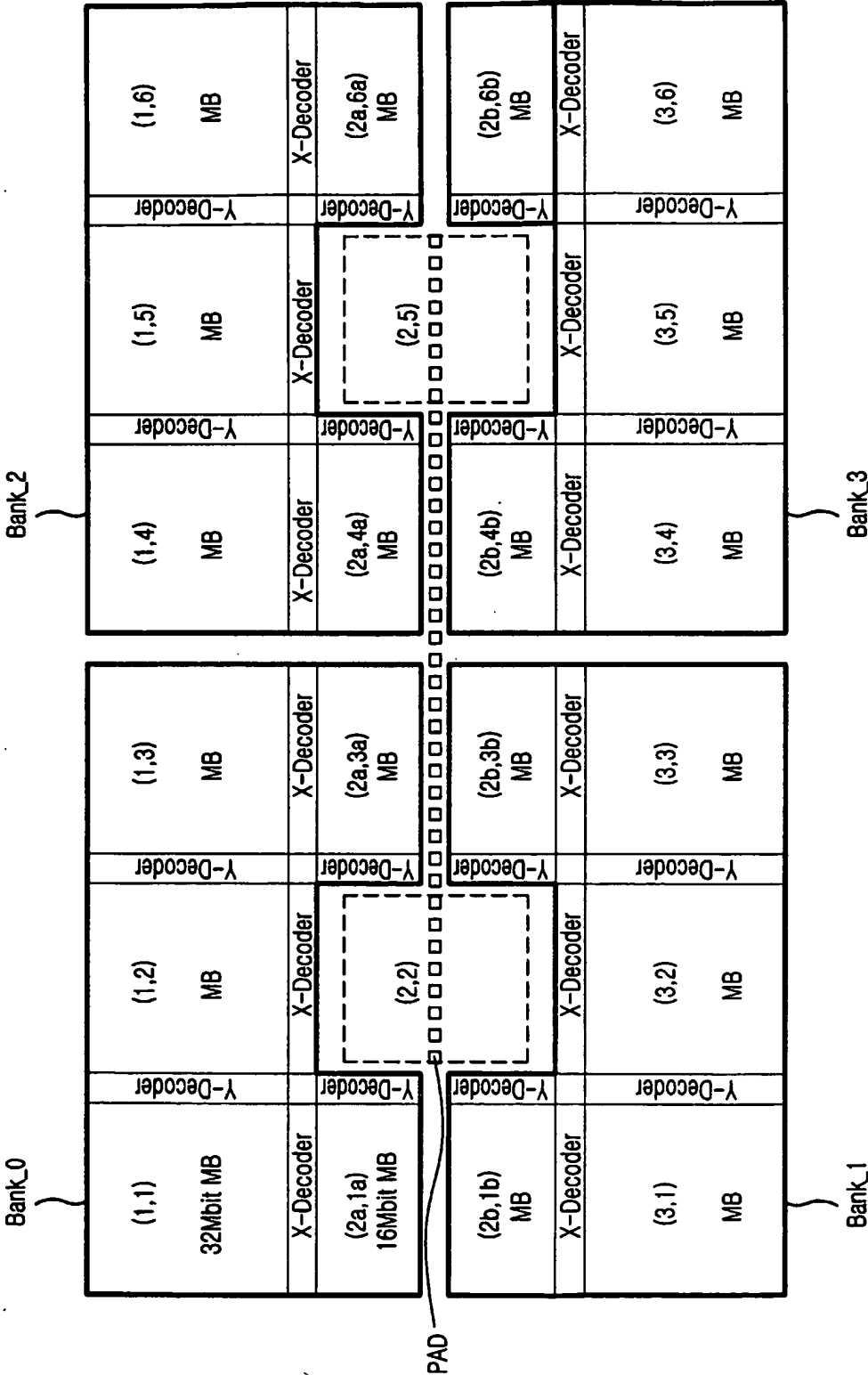


FIG. 6



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FIG. 7

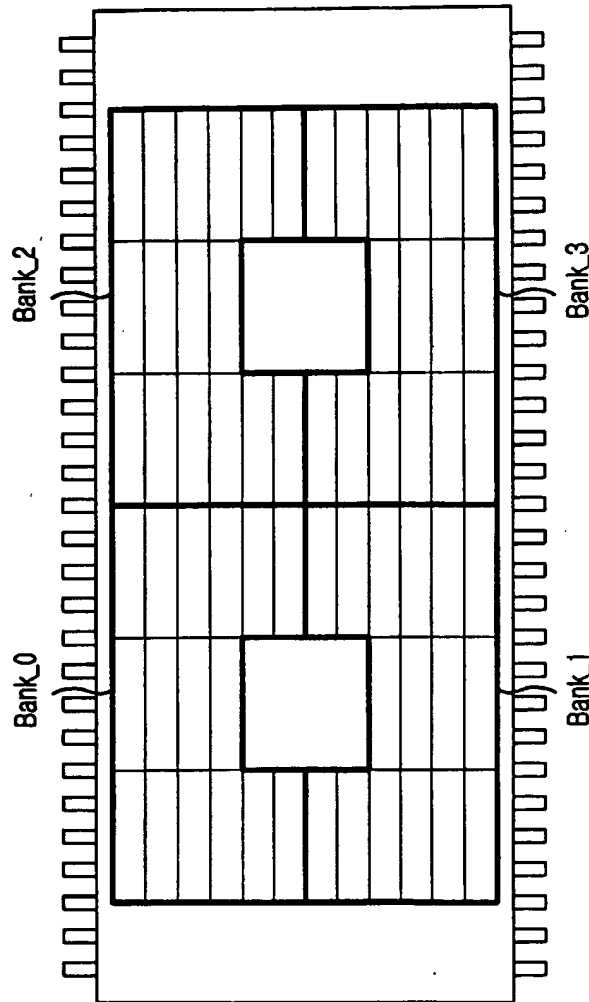
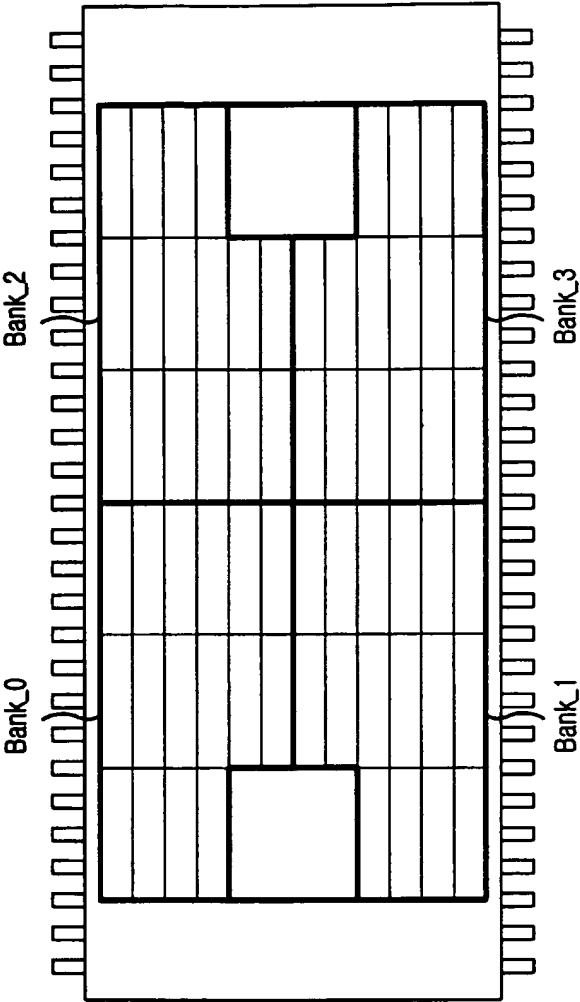
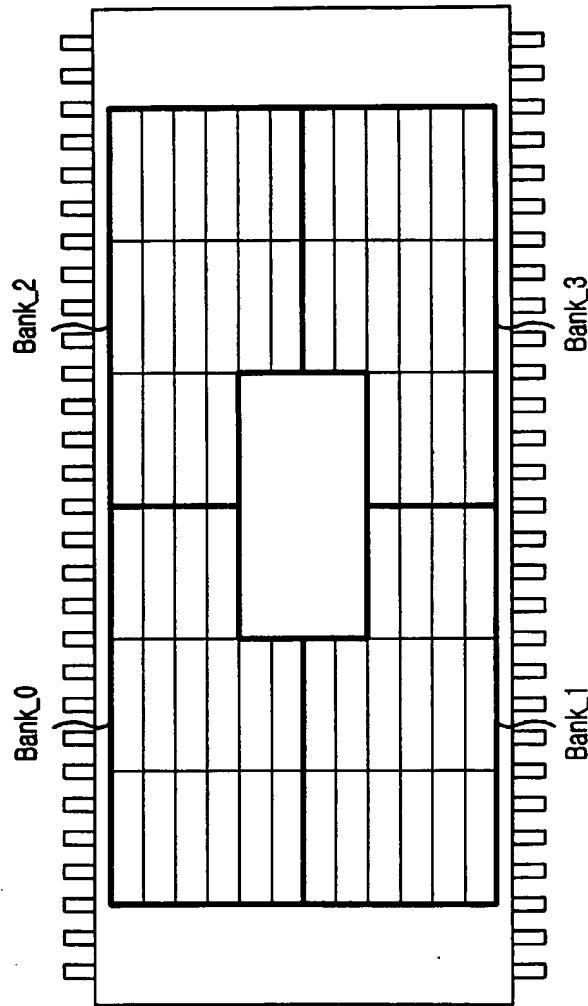


FIG. 8A



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FIG. 8B



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FIG. 9

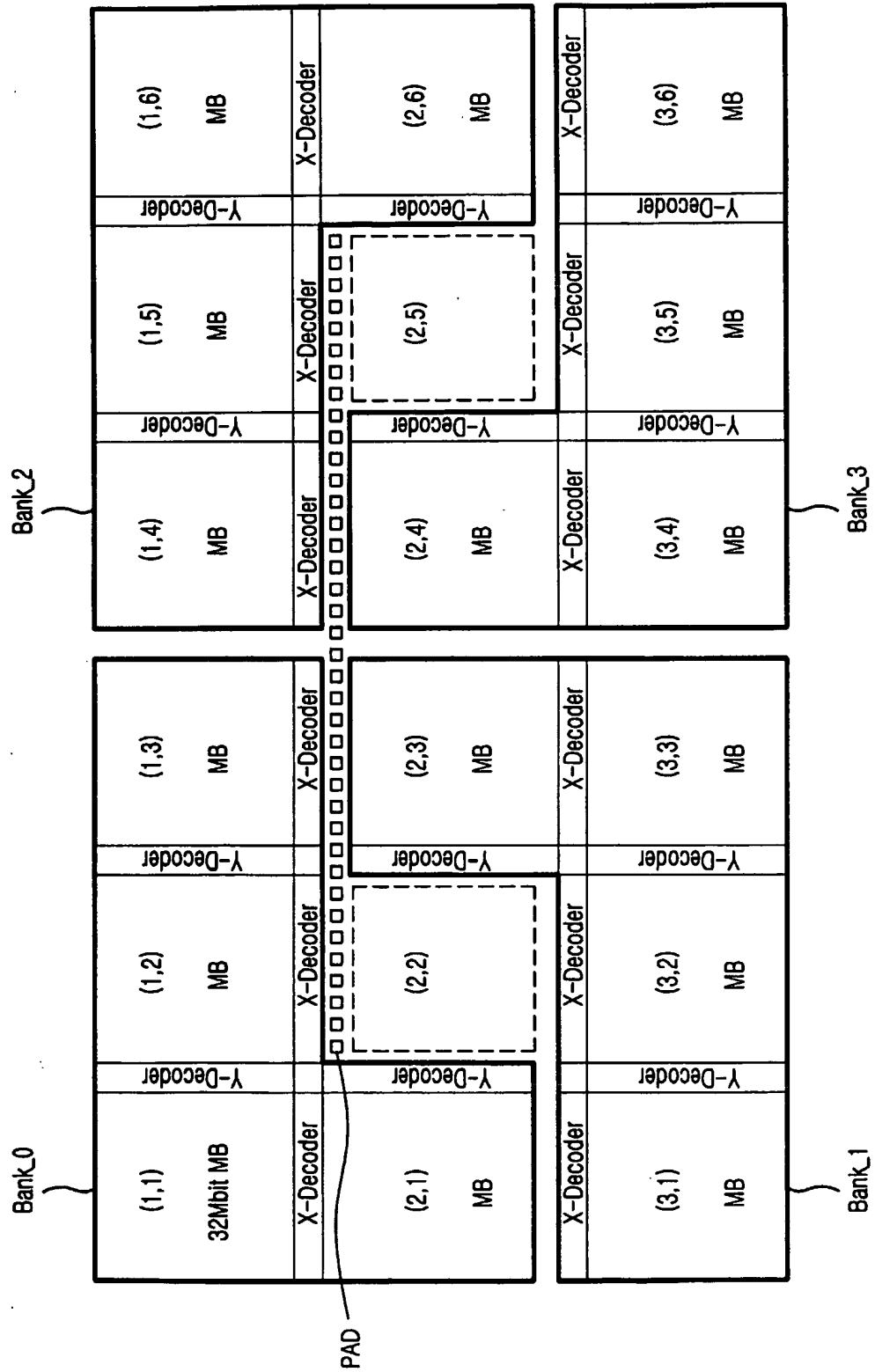


FIG. 10

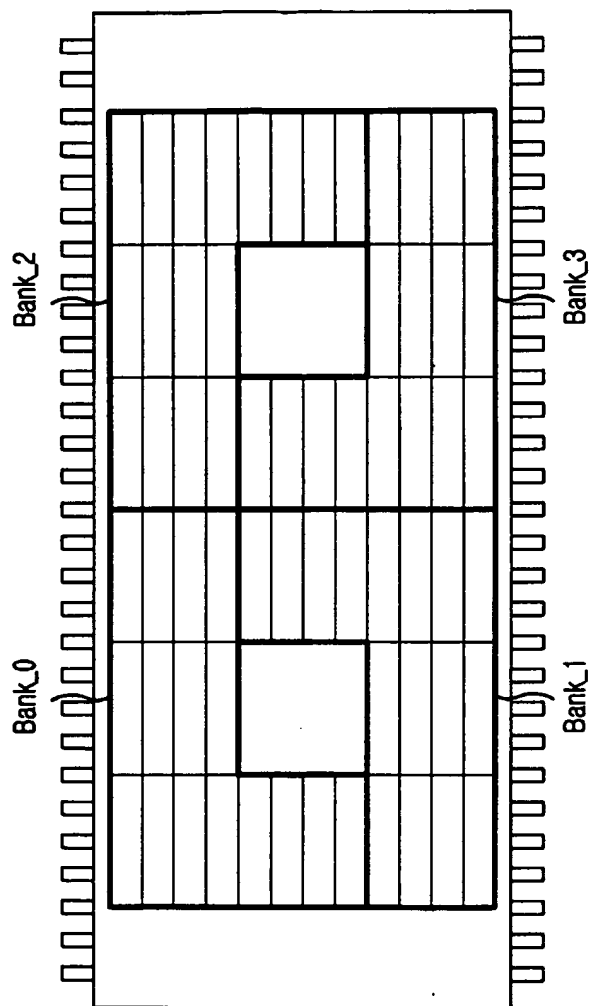


FIG. 11A

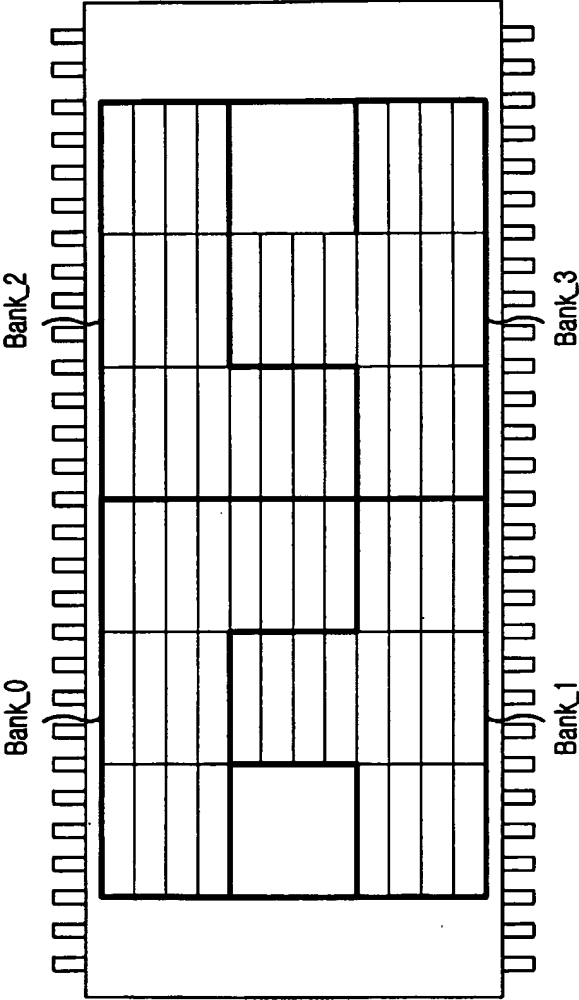
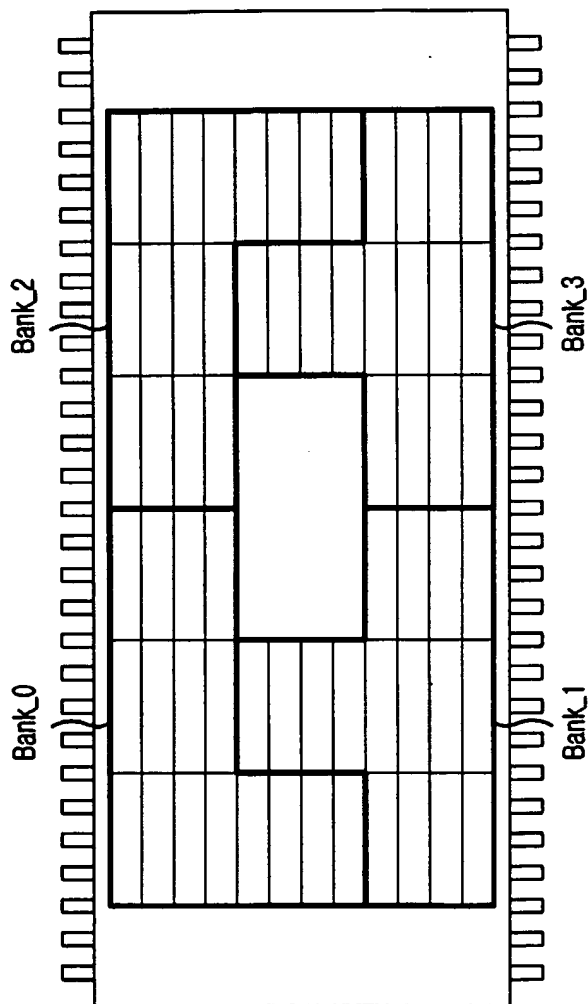


FIG. 11B



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FIG. 12

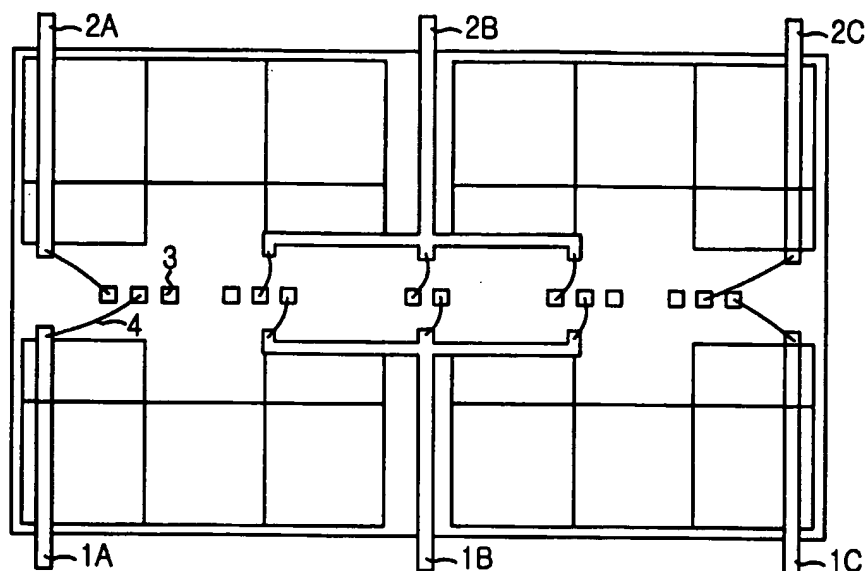


FIG. 13

